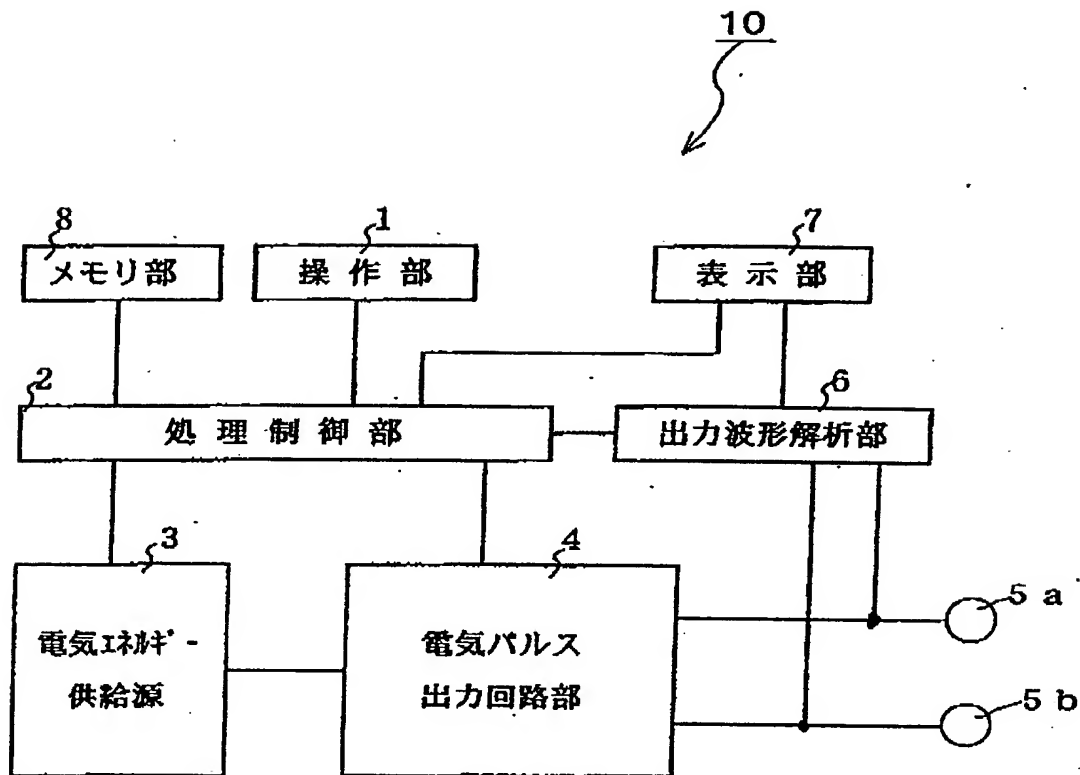
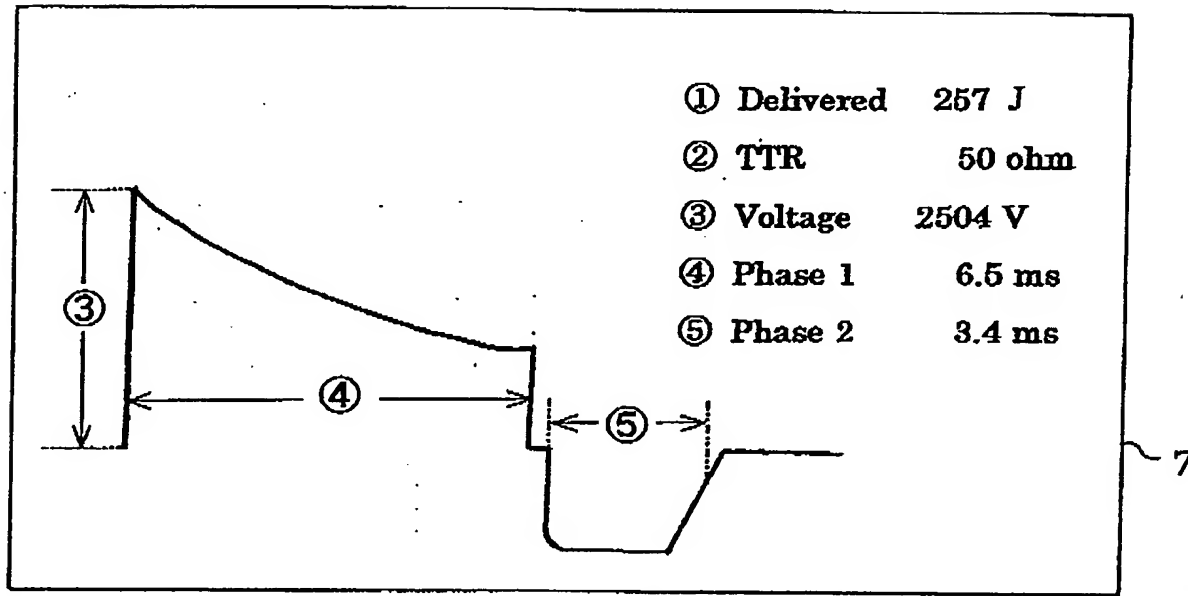


Fig. 1

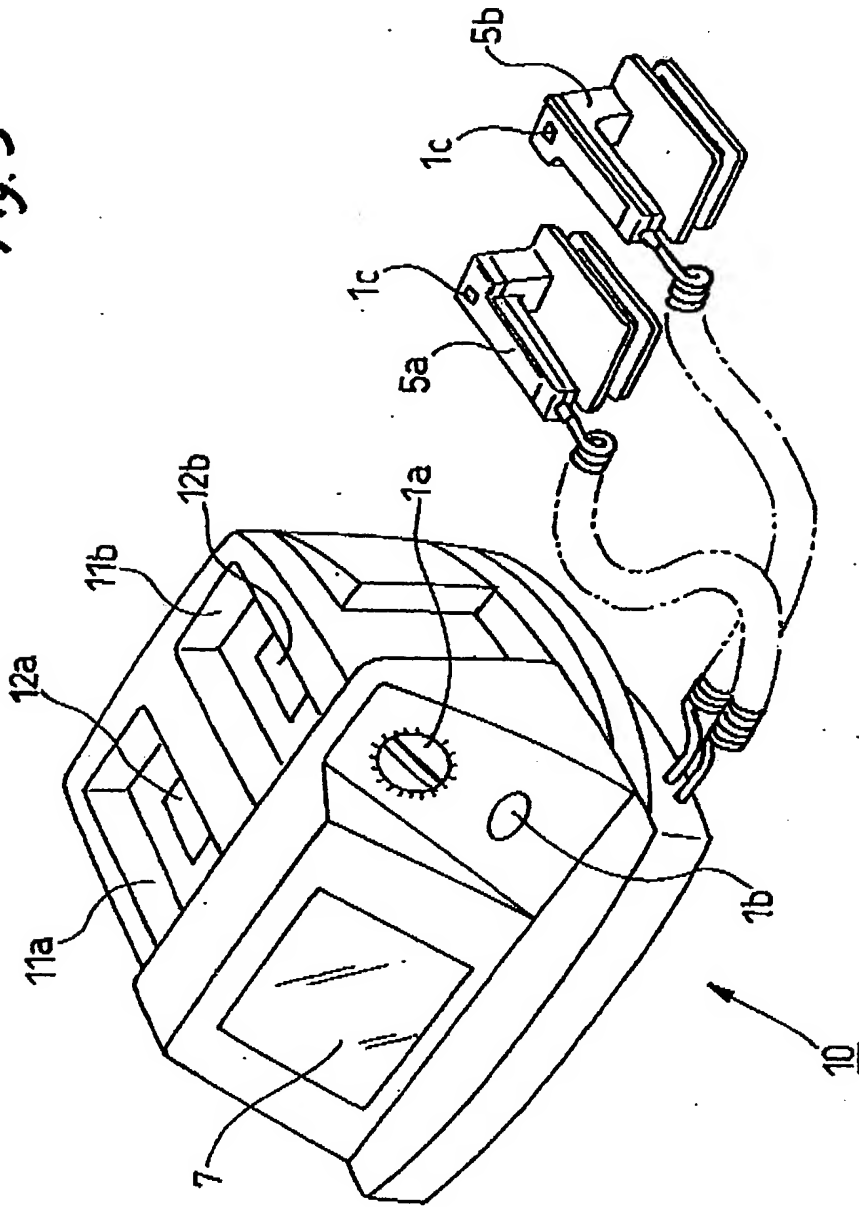


- 1: control panel
- 2: processor
- 3: power source
- 4: pulse generator
- 6: output waveform analyzer
- 7: screen
- 8: storage

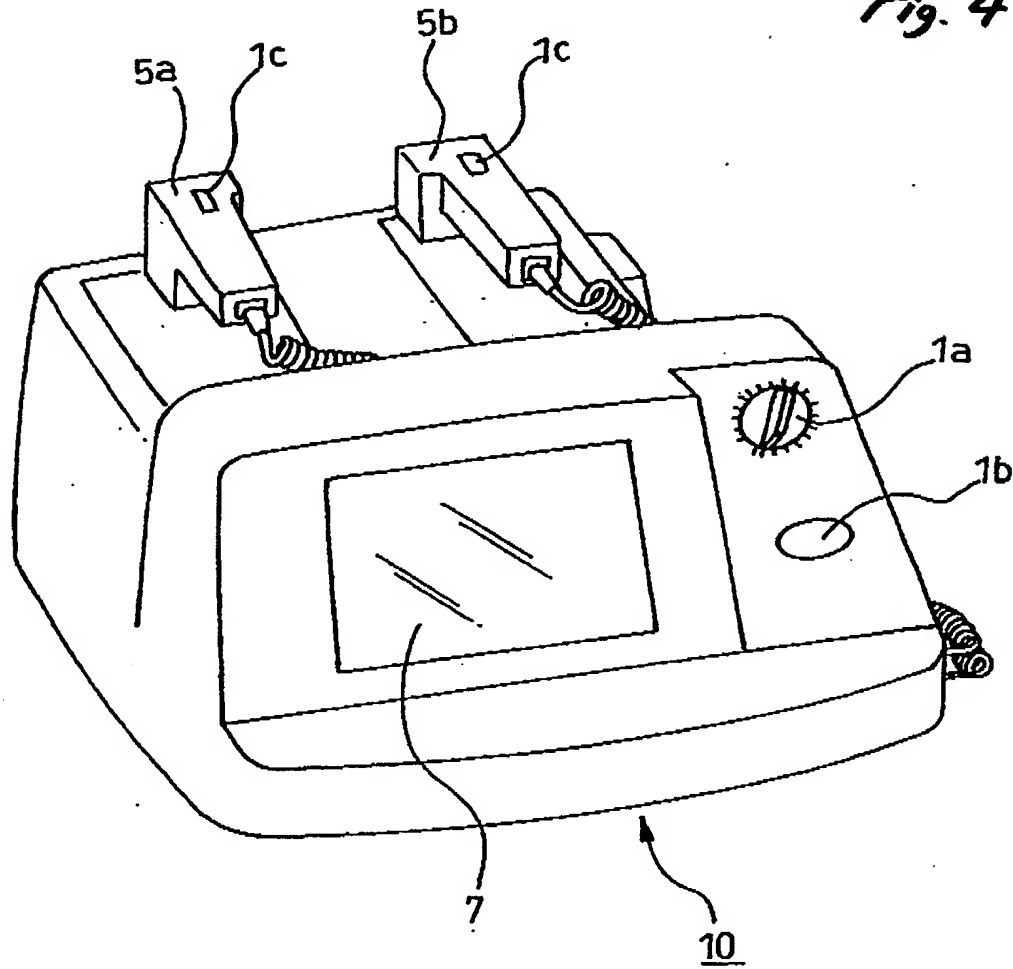


*Fig. 2*

Fig. 3



*Fig. 4*



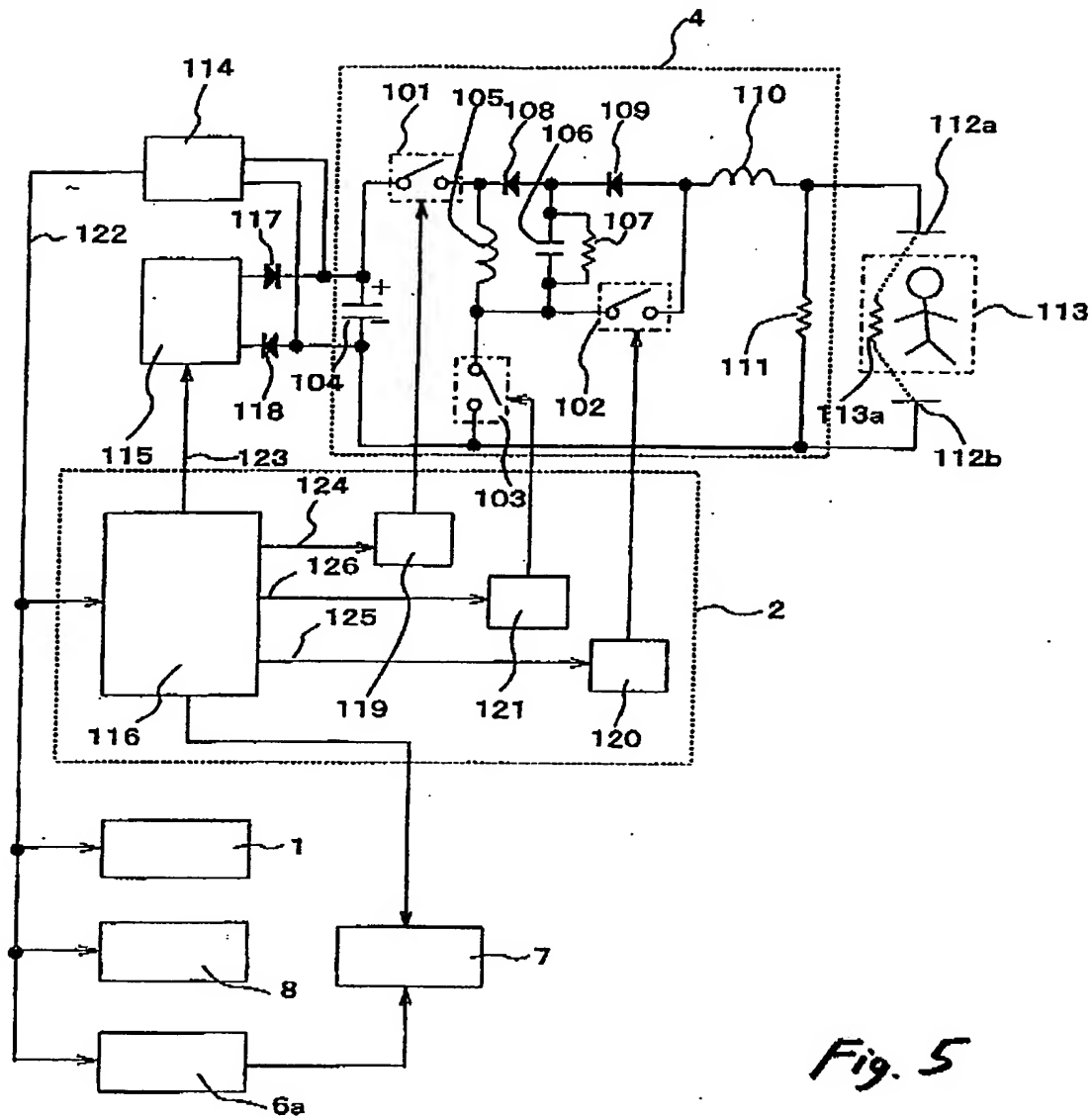


Fig. 5

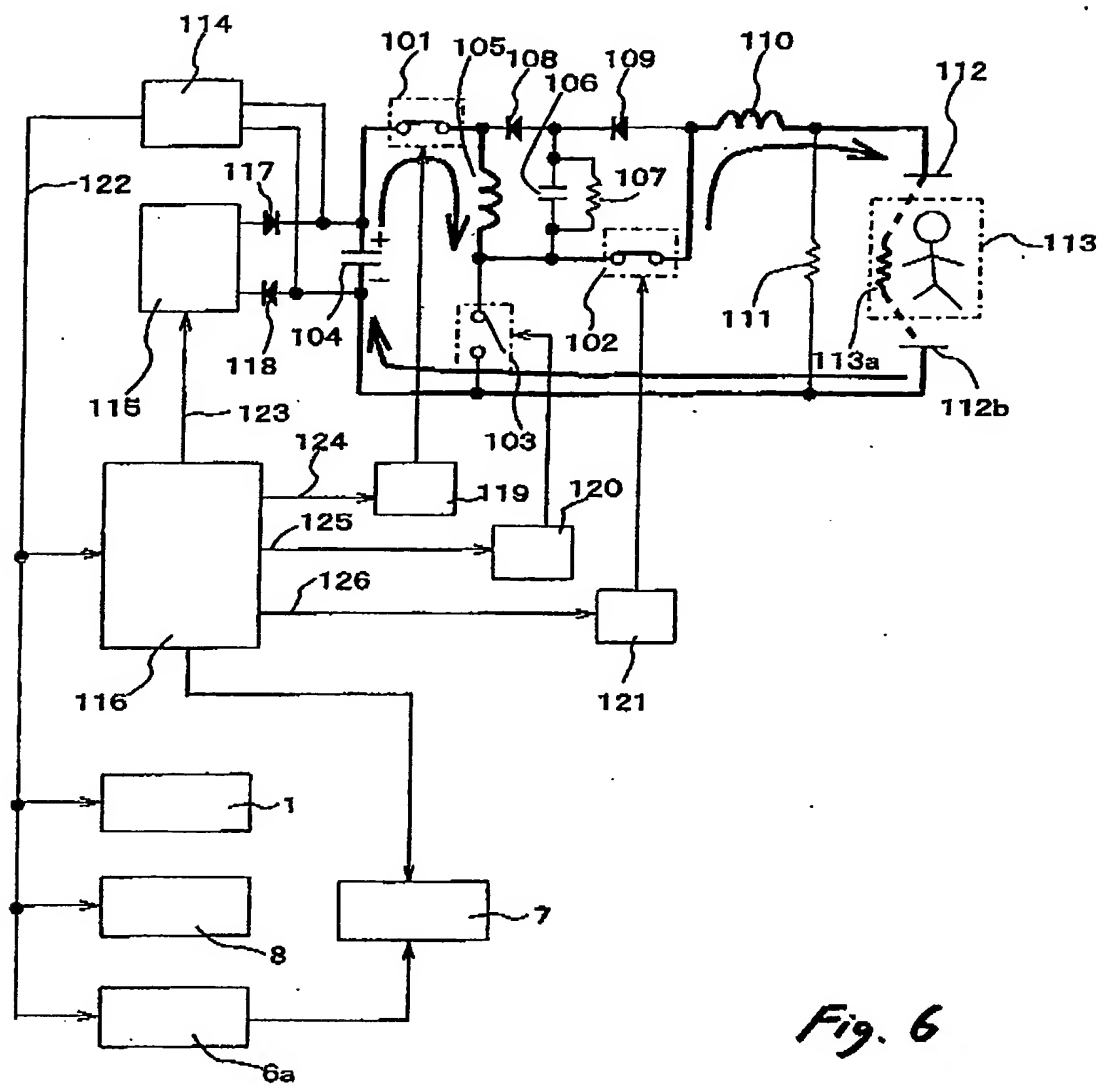


Fig. 6

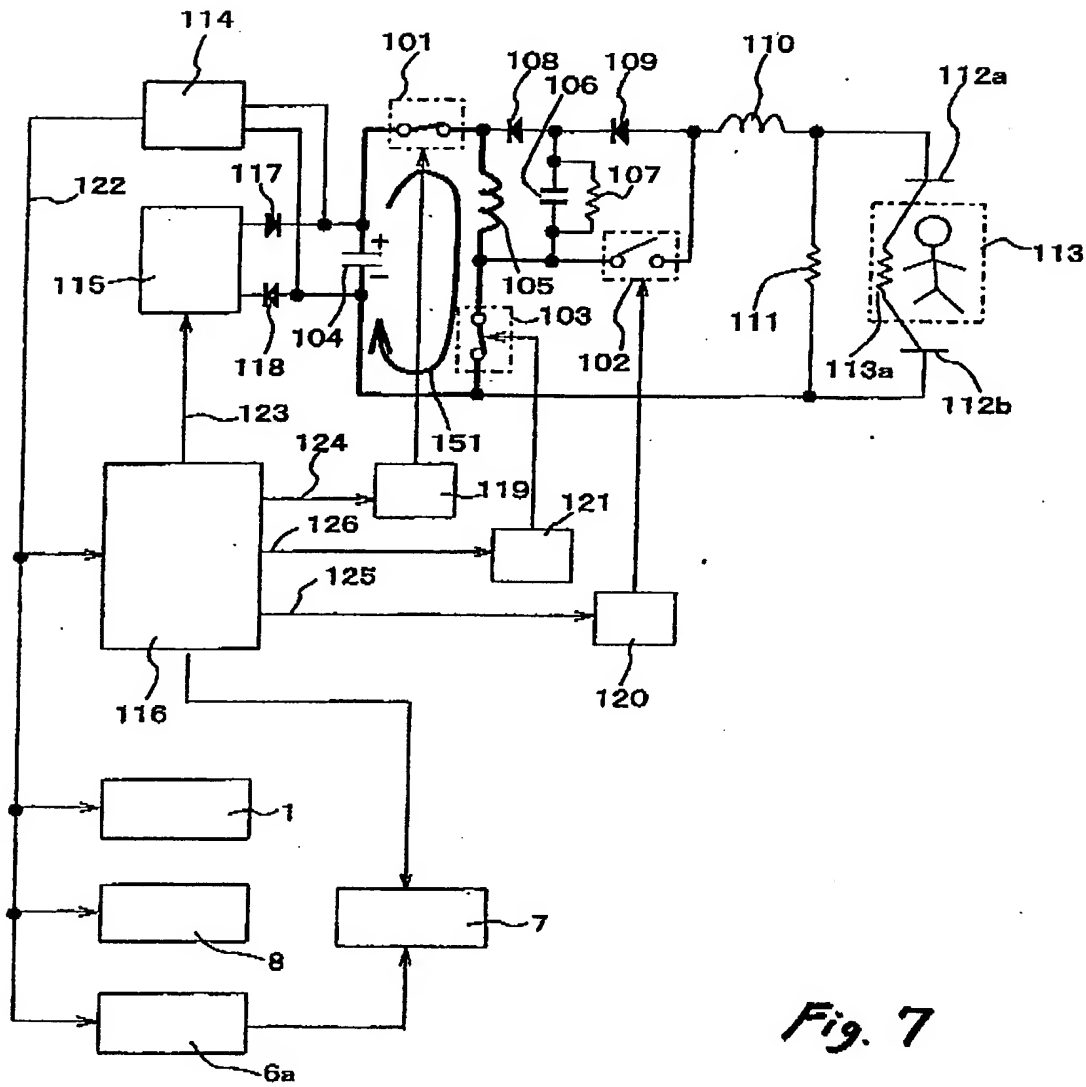


Fig. 7





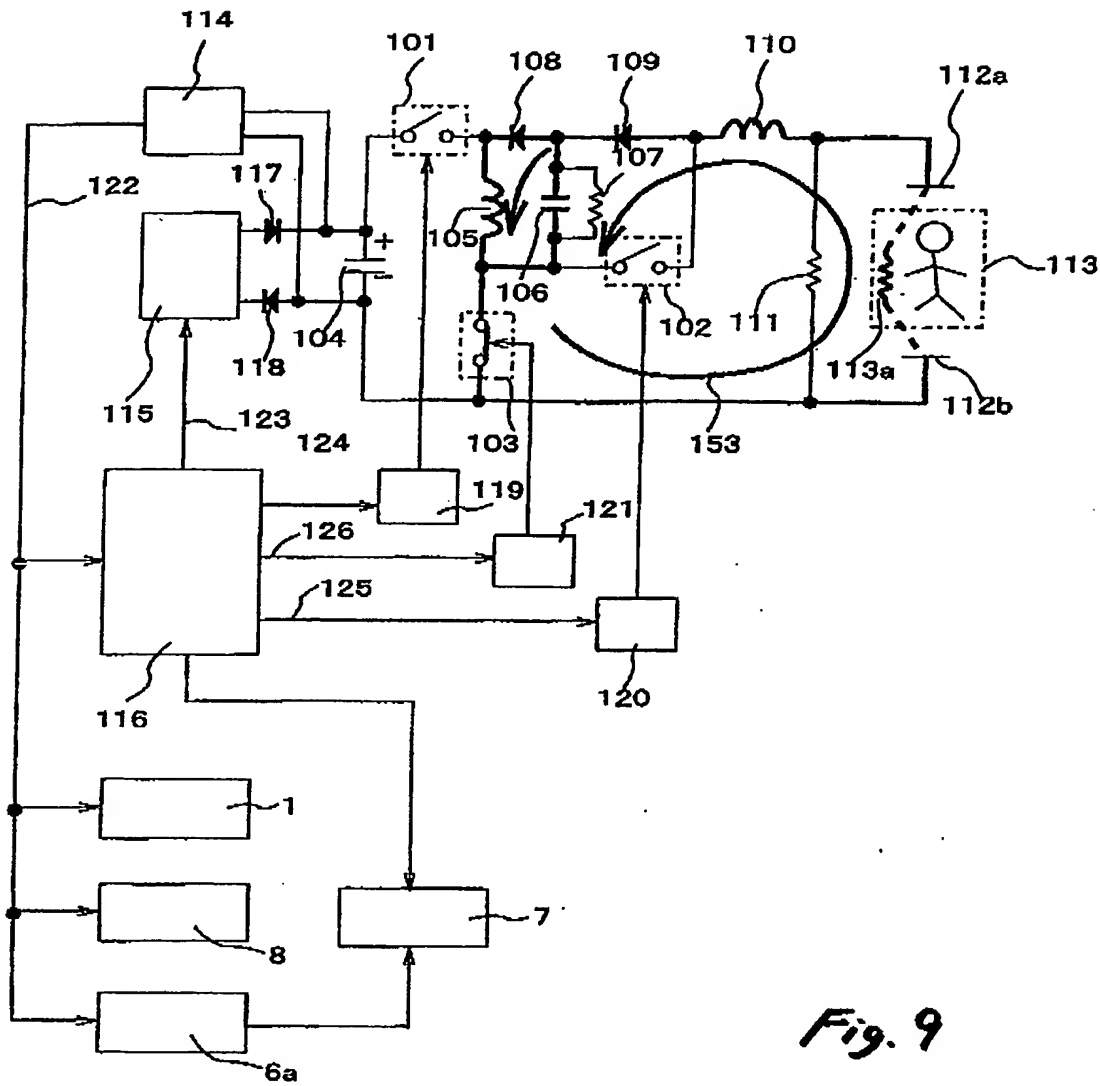
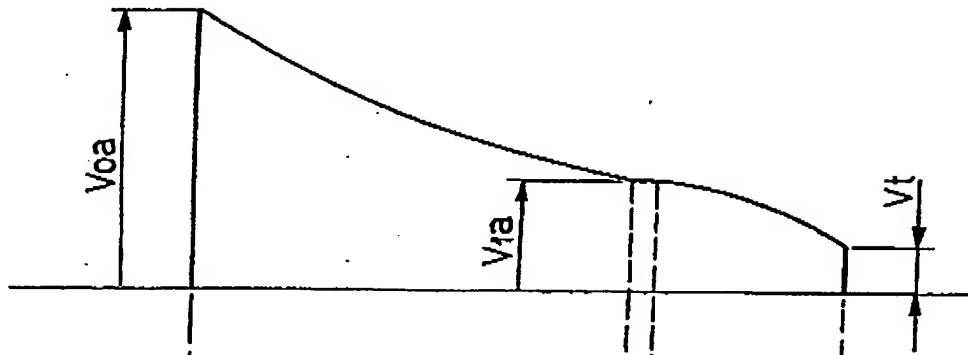


Fig. 9

*Fig. 10A*



*Fig. 10B*

